



Appl. No.

10/829,380

Confirmation No. 7082

Applicant

A. HAYASHI et al

Filed

April 22, 2004

Title

A SEMICONDUCTOR INTEGREATED CIRCUIT

CONTROLLING OUTPUT IMPEDANCE AND SLEW RATE

TC/AU

2816

Examiner

T. Lam

Docket No.

H-1139

Customer No.:

24956

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

RESPONSE TO RESTRICTION REQUIREMENT

Sir:

In response to the Restriction Requirement mailed June 17, 2005, Applicants elect to prosecute the invention defined by the Examiner as Species A - Figure 1. Claims 1-13 are readable on the elected species. This election is made with traverse.

The Commissioner is hereby authorized to charge any additional payment due to Deposit Account No. 50-1417.

Respectfully submitted,

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July 15, 2005